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**INTERESTED RESEARCH AREAS:**

Power Electronics, harmonic elimination in inverters, multi level inverters.

**PUBLICATION:**

1	N. Prashanth, B. Kumar, J. Yadagiri, A. Dasgupta, "Harmonic Minimization in Multilevel Inverter by using PSO", <i>International Journal of Recent Trends in Engineering &amp; Technology</i> , Vol 5 Issue 2 pp. 1-6
2	B Kumar, A Dasgupta, "Hardware Implementation of 3 phase inverter by SHE technique using PIC16F877A microcontroller", <i>National Seminar on "Applied Power Electronics, (APE-2009) held during at NIT Rourkela. 30 January- 1 February, 2008</i>
3.	Bijayalaxmi Patra, Brijesh Kumar, J. Yadagiri and A. Dasgupta, "Estimation of Switching angles by using PSO of three phase voltage source inverter", <i>3rd International conference on computer modeling and simulation. January 7-9, 2009, Mumbai, V2, P33</i>
4	Bijayalaxmi Patra, Brijesh Kumar, J.Yadagiri and A.Dasgupta, "Estimation of Switching angles by using PSO of three phase voltage source inverter", <i>International Journal of Modeling and Optimization (Accepted for publication 2012, paper selected from ICCMS 2009)</i>
5.	R Shah, A Dasgupta, "Micro controller Based Controlled DC Drive", <i>All India Seminar conducted by Institute of Engineers India, at Nalco, Angul, November 2006</i>
6	A Dasgupta, "Current Trend of Power Semiconductor Devices", <i>All India seminar on "Challenges of Power sector in 21st Century", 15-16 March2008, Organized by Institution of Engineers India, Orissa State Centre</i>
7	Brijesh Kumar, Abhijit Dasgupta, Bijayalaxmi Patra And J. Yadgiri, "Hardware implementation of three-phase VSI using PIC16F877A microcontroller and optimization using PSO", <i>International Journal of Power and Energy Conversion, 4 (2), 182-192, Inderscience Publishers, 2013</i>
8	Madhichetty Sreedhar , A. Dasgupta, "An Experimental Verification of Performance Improvement In Inverter Design By Using Accelerated Particle Swarm Optimization Technique", <i>International Journal of Engineering Research &amp; Technology (IJERT), Volume 1, Issue 7, September 2012</i>
9	Madhichetty Sreedhar, A Dasgupta, "A Review of Optimized Heating Performance of Induction Cookers", <i>International Journal of Power System Operation and Energy Management, Vol-1, Issue-3, 2012, page 64-66</i>
10	Madhichetty Sreedhar, A Dasgupta, "Experimental Verification of Minority Charge Carrier Inspired Algorithm Applied To Voltage Source Inverter", <i>IEEE 5th india International Conference on Power Electronics, January 2013</i>
11	Madhichetty Sreedhar, A Dasgupta, "Application of Accelerated Particle Swarm Optimization To Multi Level Diode Clamped Inverter", <i>IEEE 5th India International Conference on Power Electronics, January 2013</i>
12	Madhichetty Sreedhar, A Dasgupta, B V V S Kumar, " Application of SHE Technique to Three Level Inverter By Using Simulated Annealing" <i>International Review on Applied Engineering Reaserach, Volume 3, Issue 6, Pages 16-20, PublisherResearch India Publication ISSN:2248—967, Publication date 2013/7/27</i>

13	Sreedhar Madichetty, Susmita Panda, Sambeet Mishra, Abhijit Dasgupta "A Review and Advance Technology in Multi-Area Automatic Generation Control by Using Minority Charge Carrier Inspired Algorithm", International Journal of Emerging Electric Power Systems", Volume 14, Issue 6, Pages 609–627, ISSN (Online) 1553-779X, ISSN (Print) 2194-5756, DOI: 10.1515/ijeeps-2013-0103, November 2013
14	Sreedhar Madichetty and Abhijit Dasgupta , "Modular Multilevel Converters PartI: A Review on Topologies Modulation Modeling and Control Schemes", International Journal of Power Electronics and Drive Systems (JPEDS)", volume-4, number -1, 2014
15	Sreedhar Madichetty and Abhijit Dasgupta , "Harmonic Mitigated Front End 3 Level Diode Clamped High Frequency Link Inverter by Using MCI Technique", International Journal of Power Electronics and Drive Systems (JPEDS)", volume-4, number -1, 2014
16	Sreedhar Madichetty and Abhijit Dasgupta , "Experimental Verification of Circulating Current Mitigation Scheme in MMC by Using ISE Technique", TELKOMNIKA Indonesian Journal of Electrical Engineering, volume = 12, number = 5, 2014